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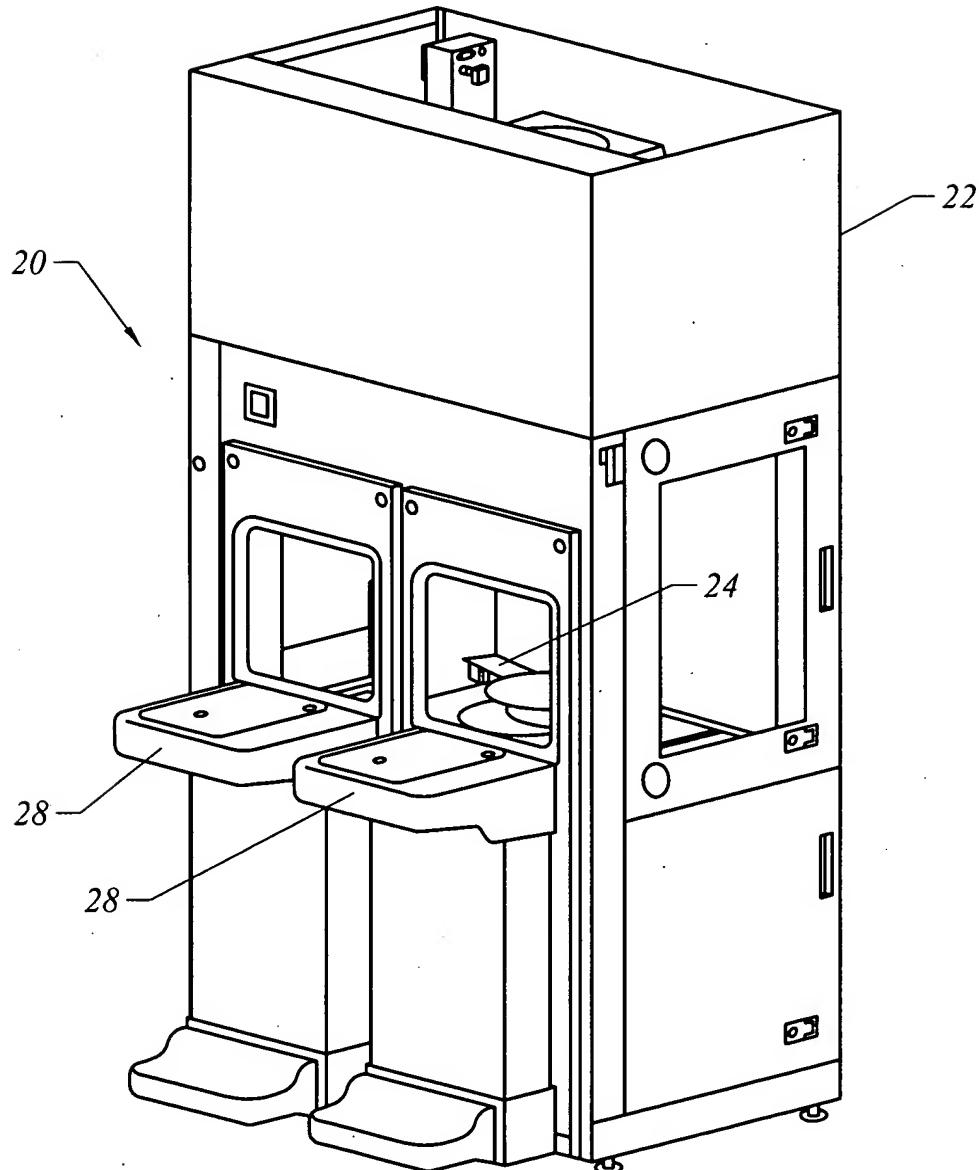
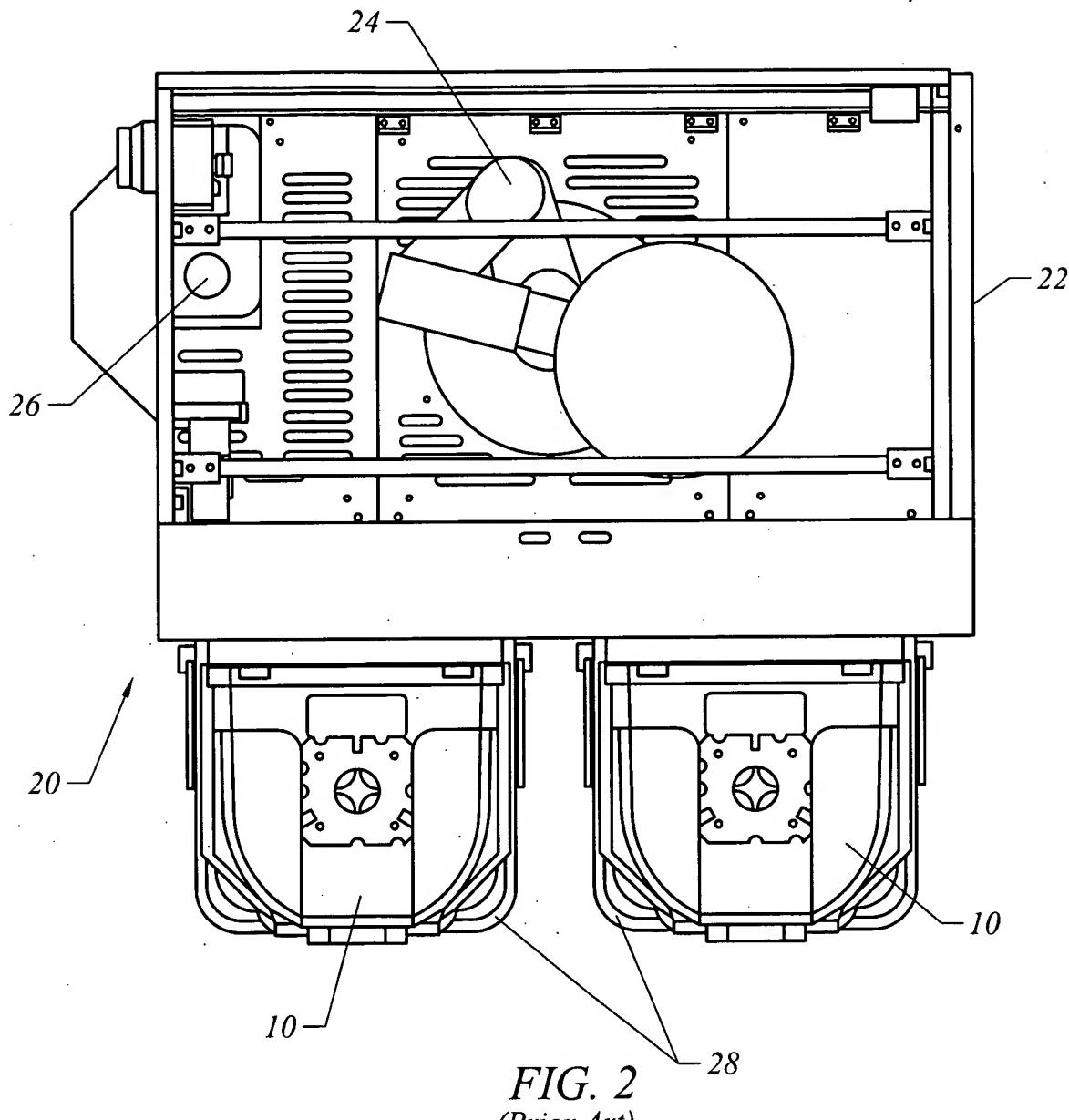


FIG. 1
(Prior Art)



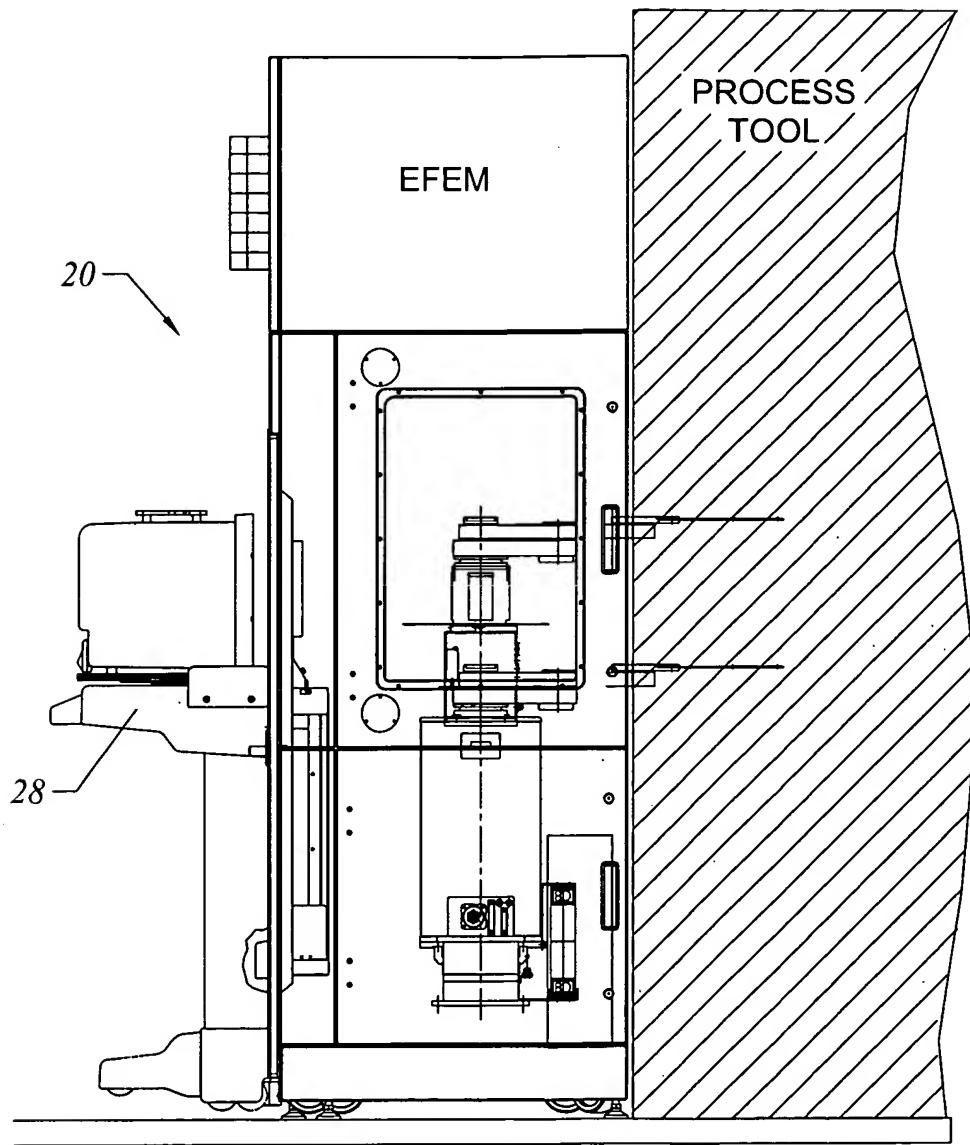


FIG. 3
(*Prior Art*)

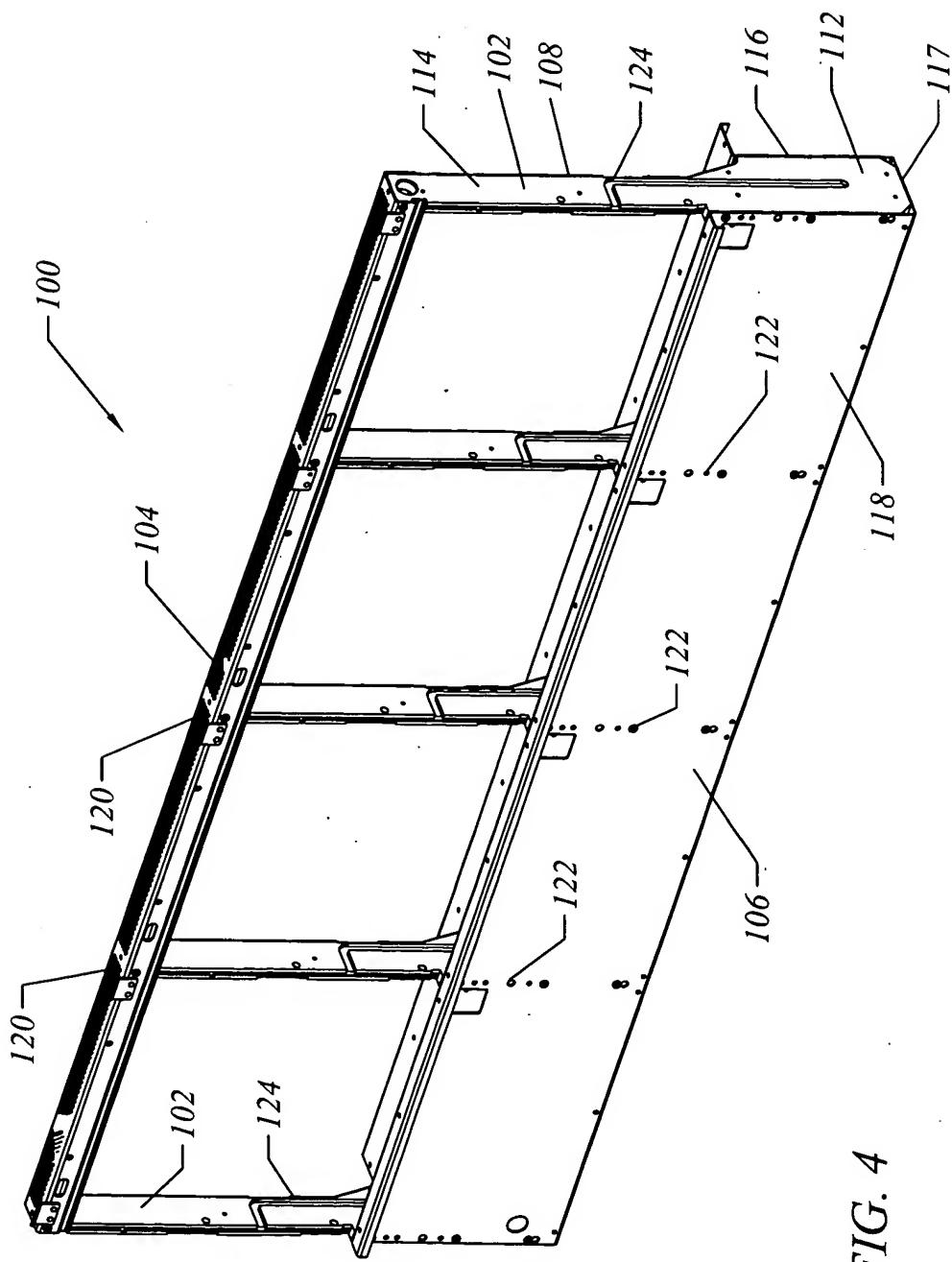
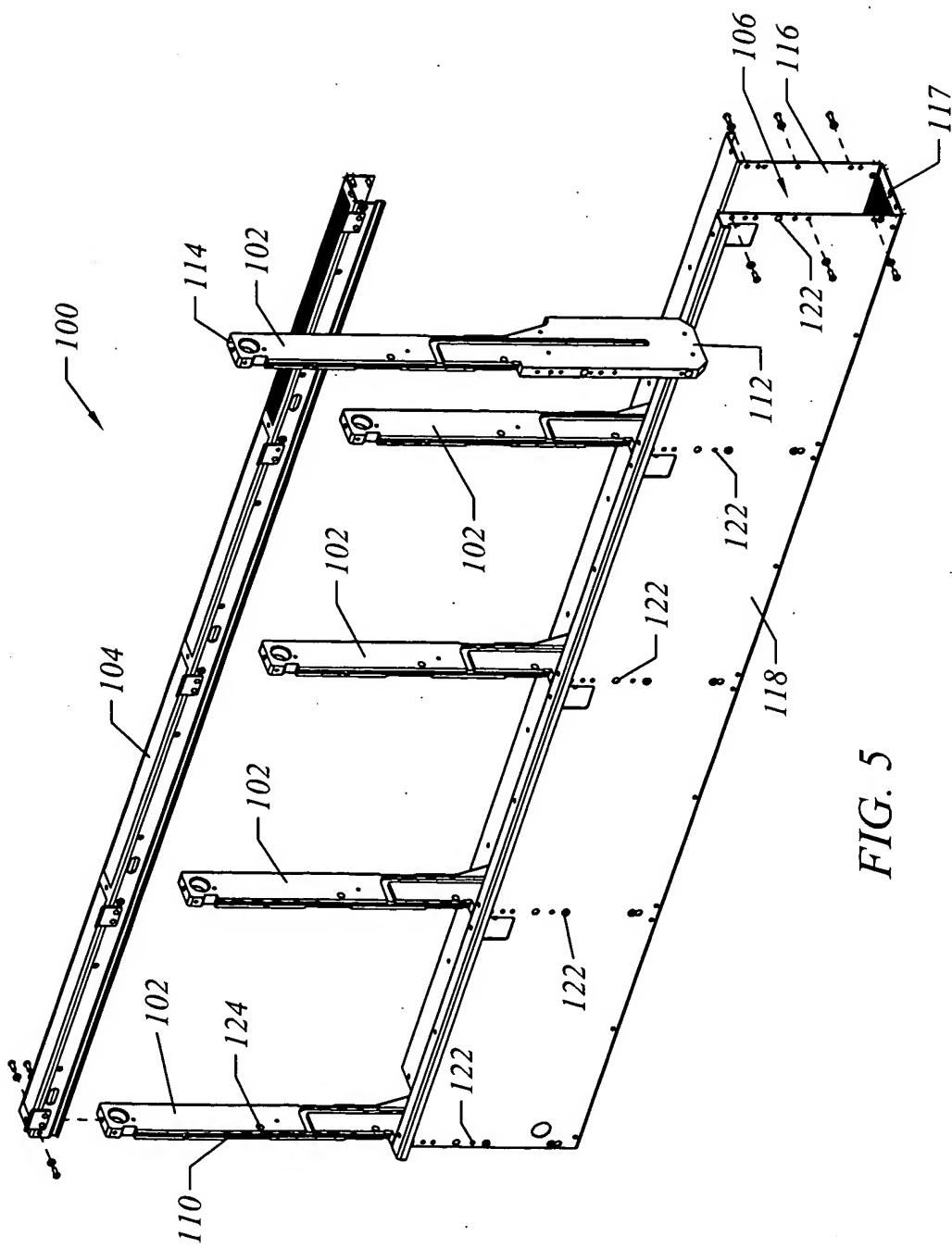


FIG. 4



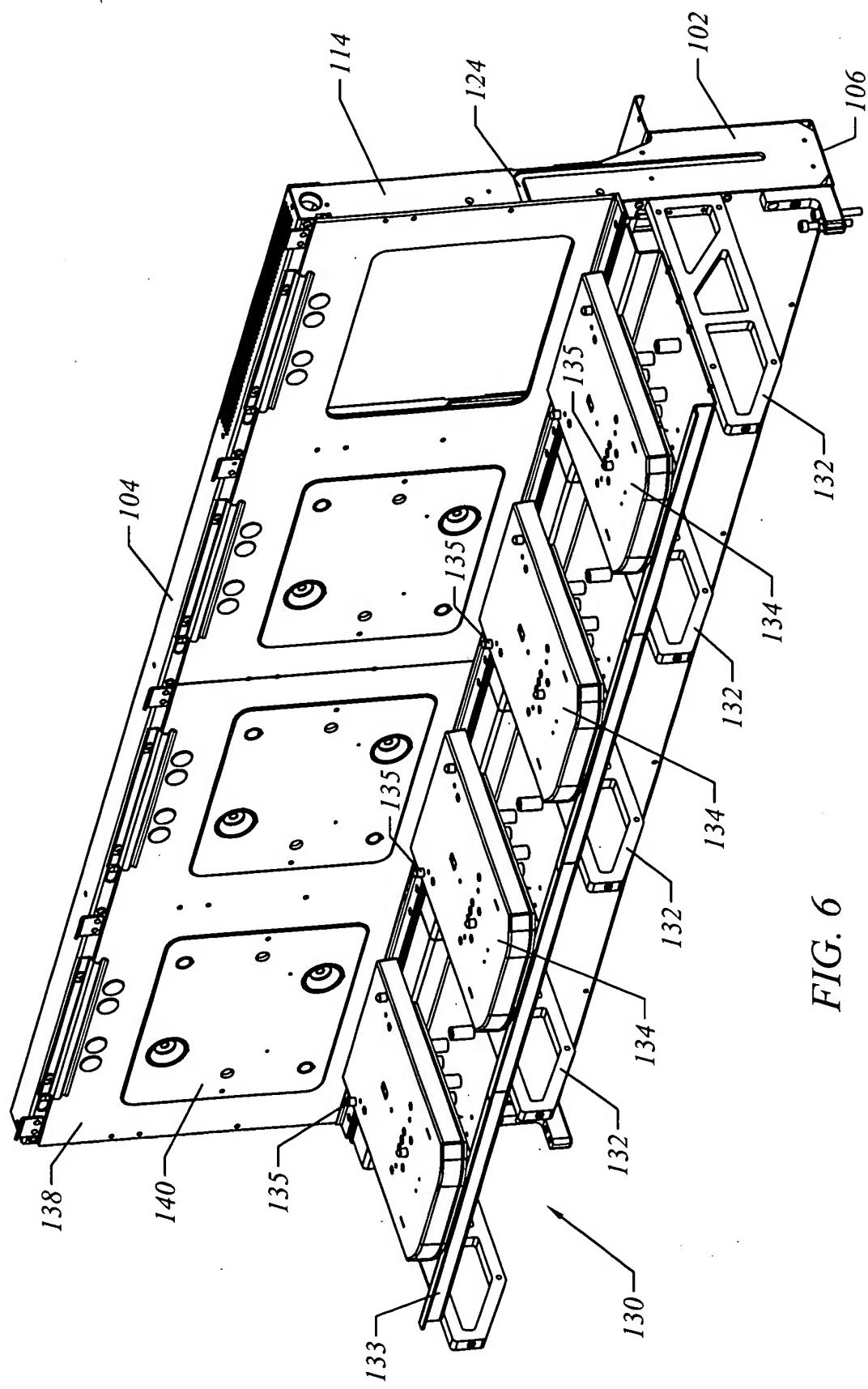


FIG. 6

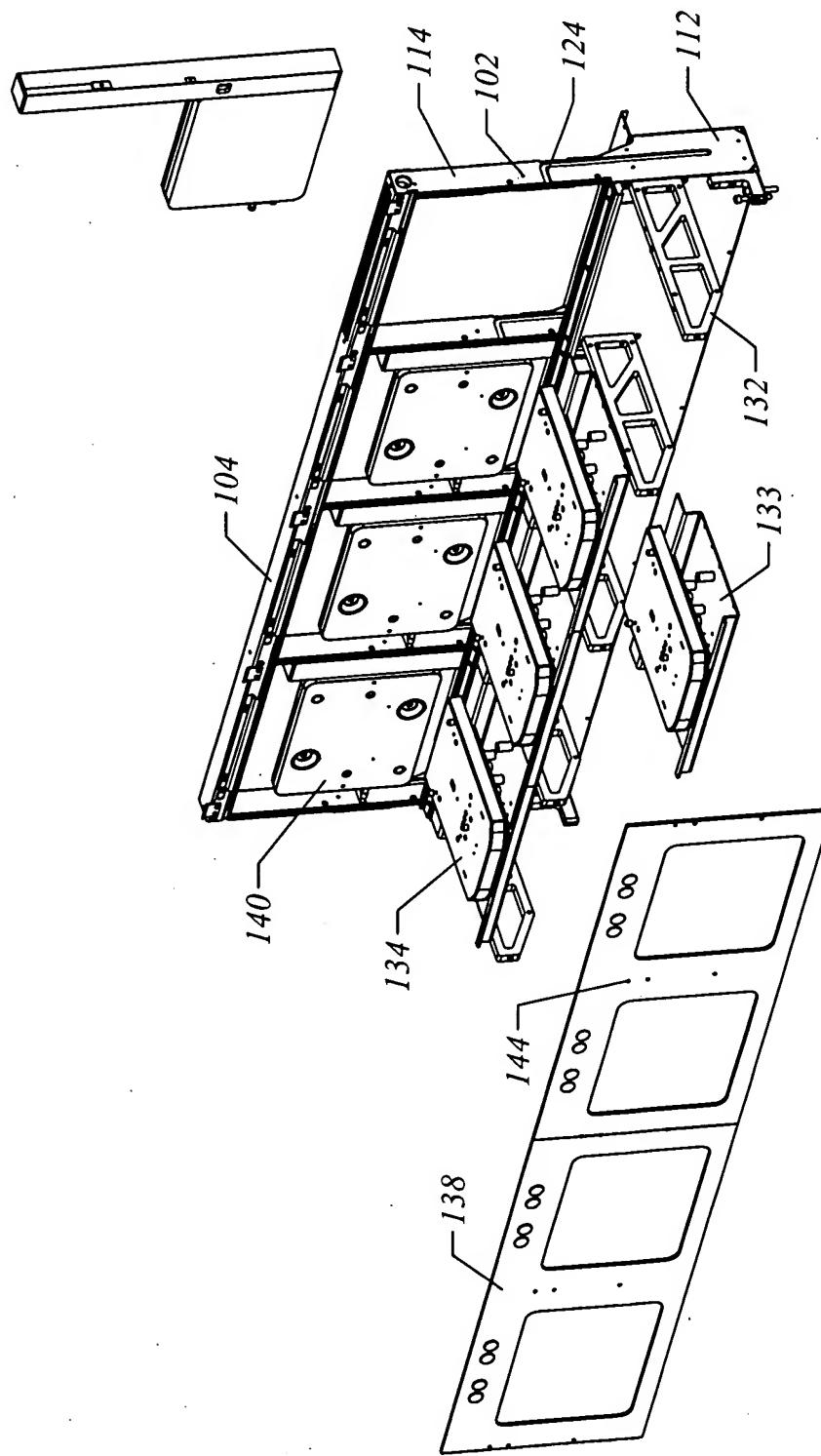


FIG. 7

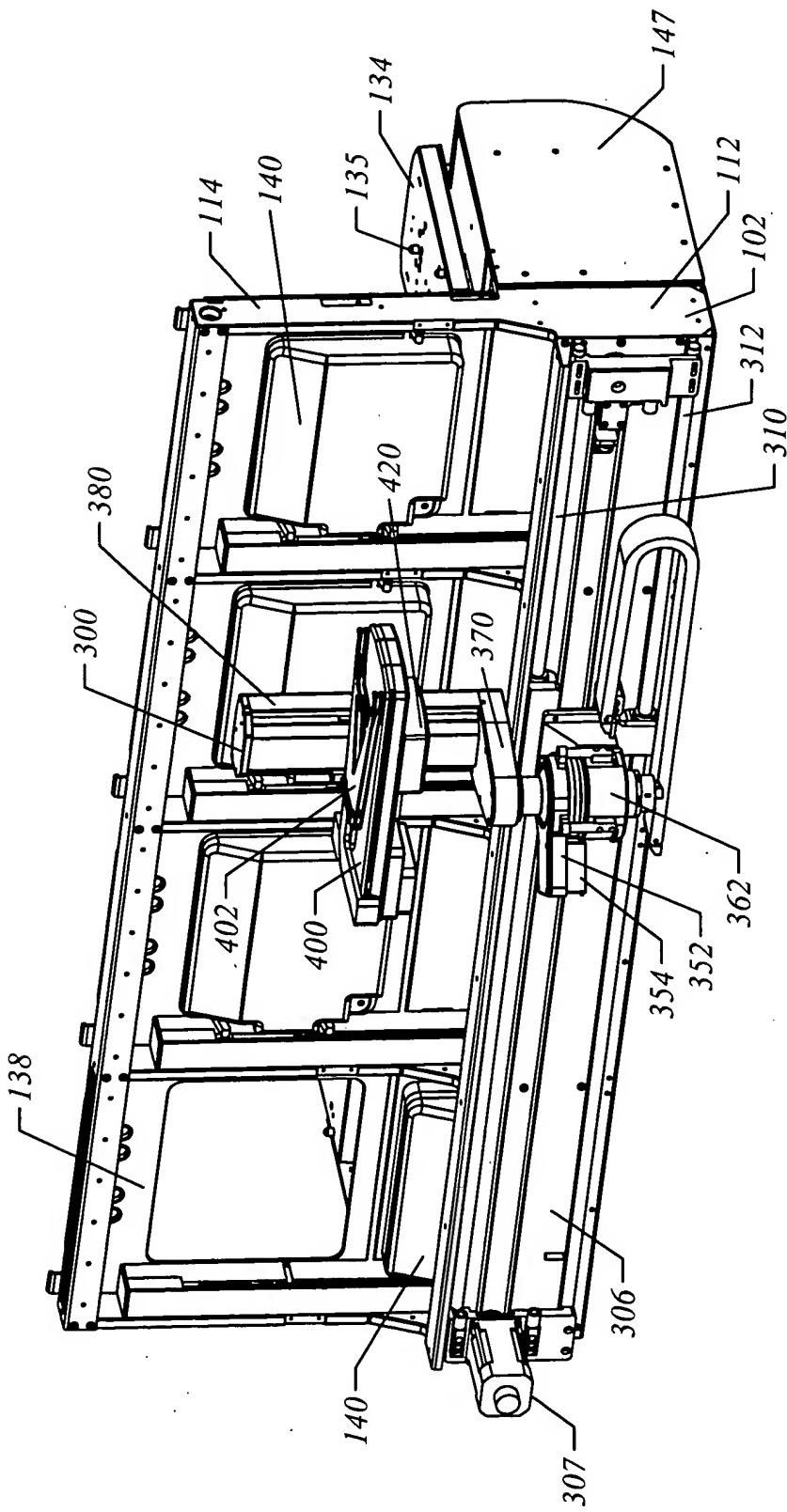


FIG. 8

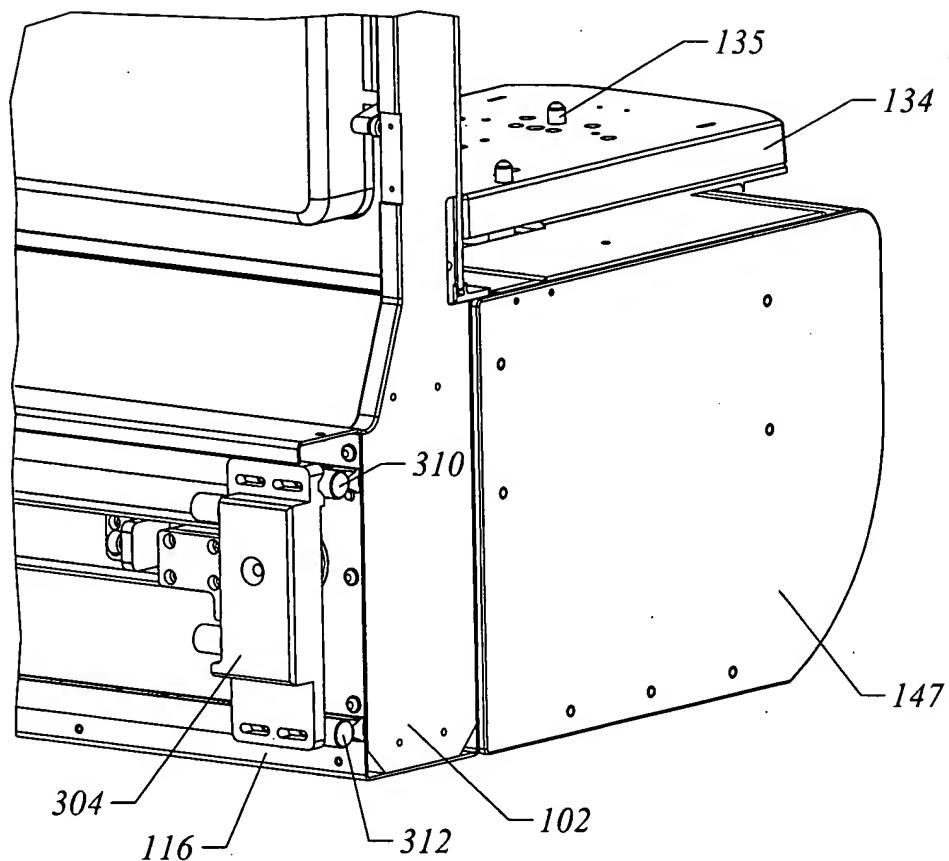


FIG. 9

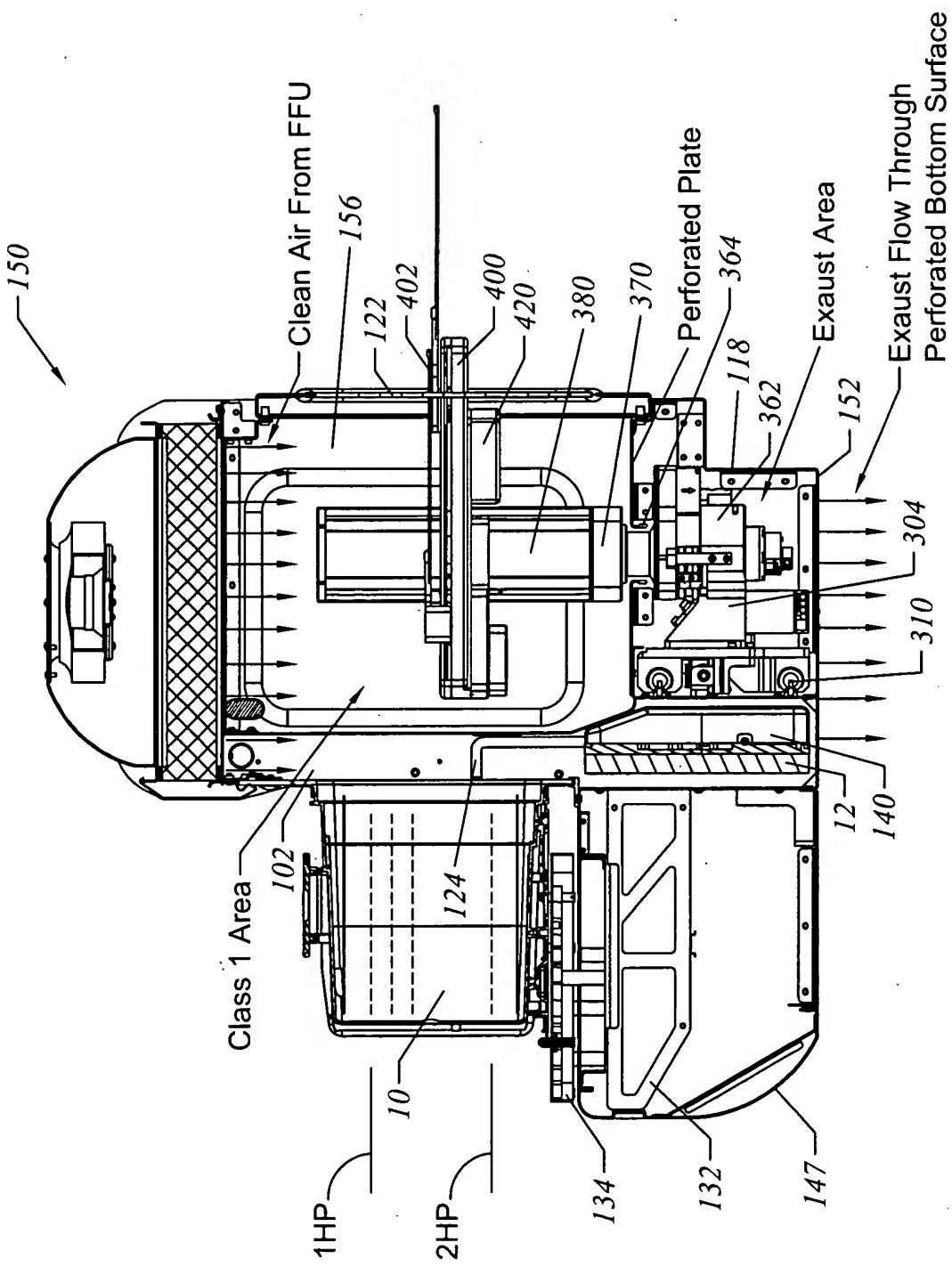


FIG. 10

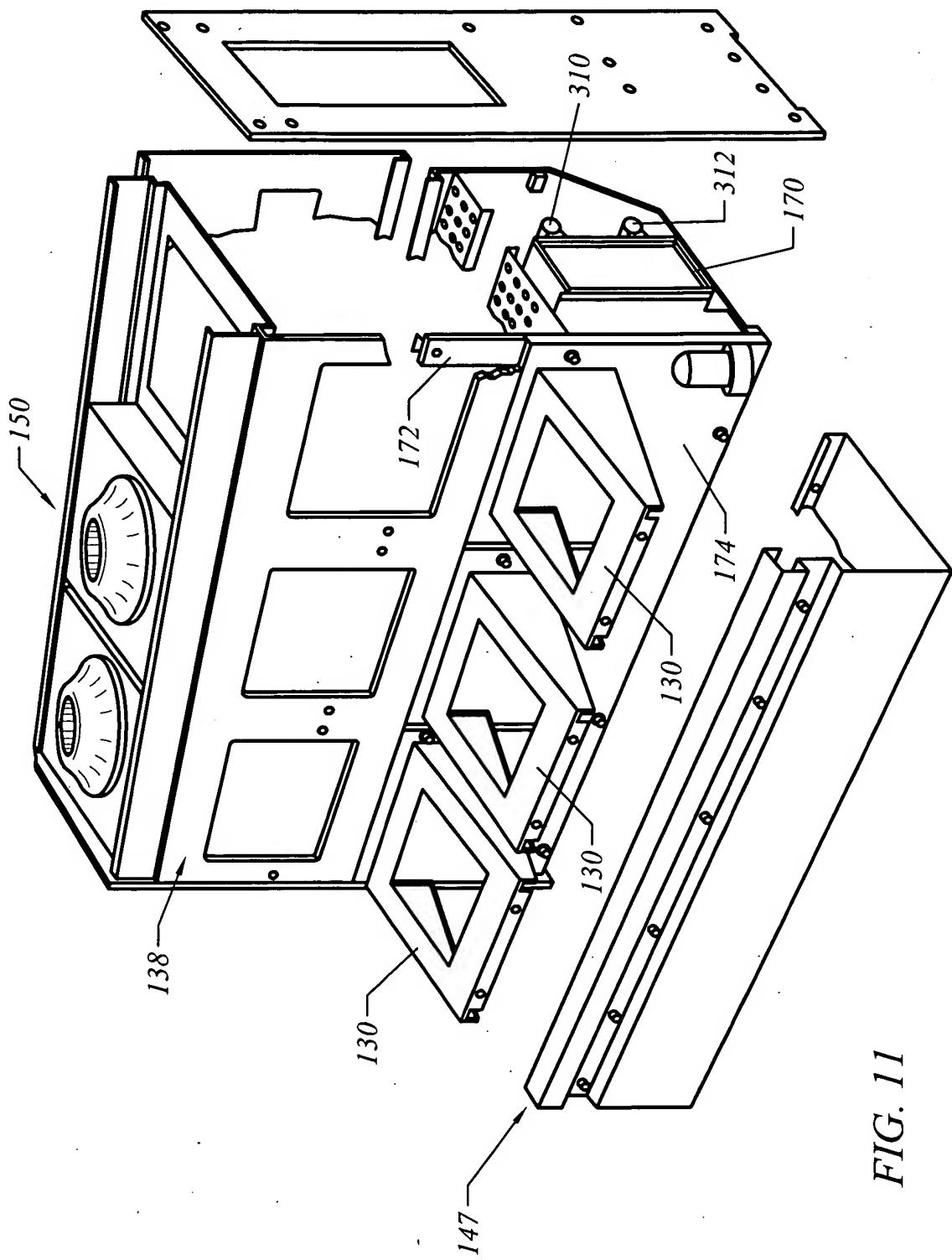


FIG. 11

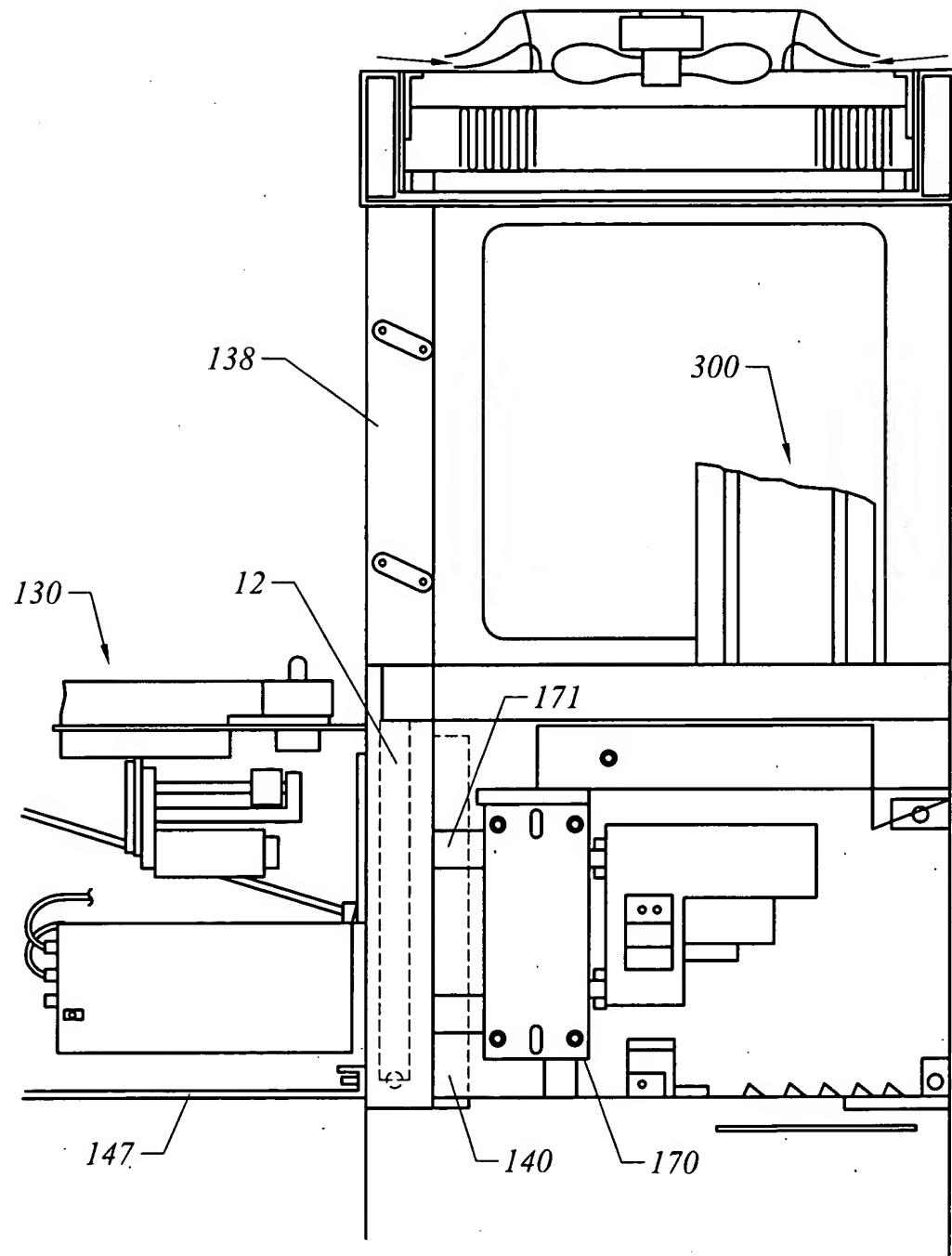


FIG. 12

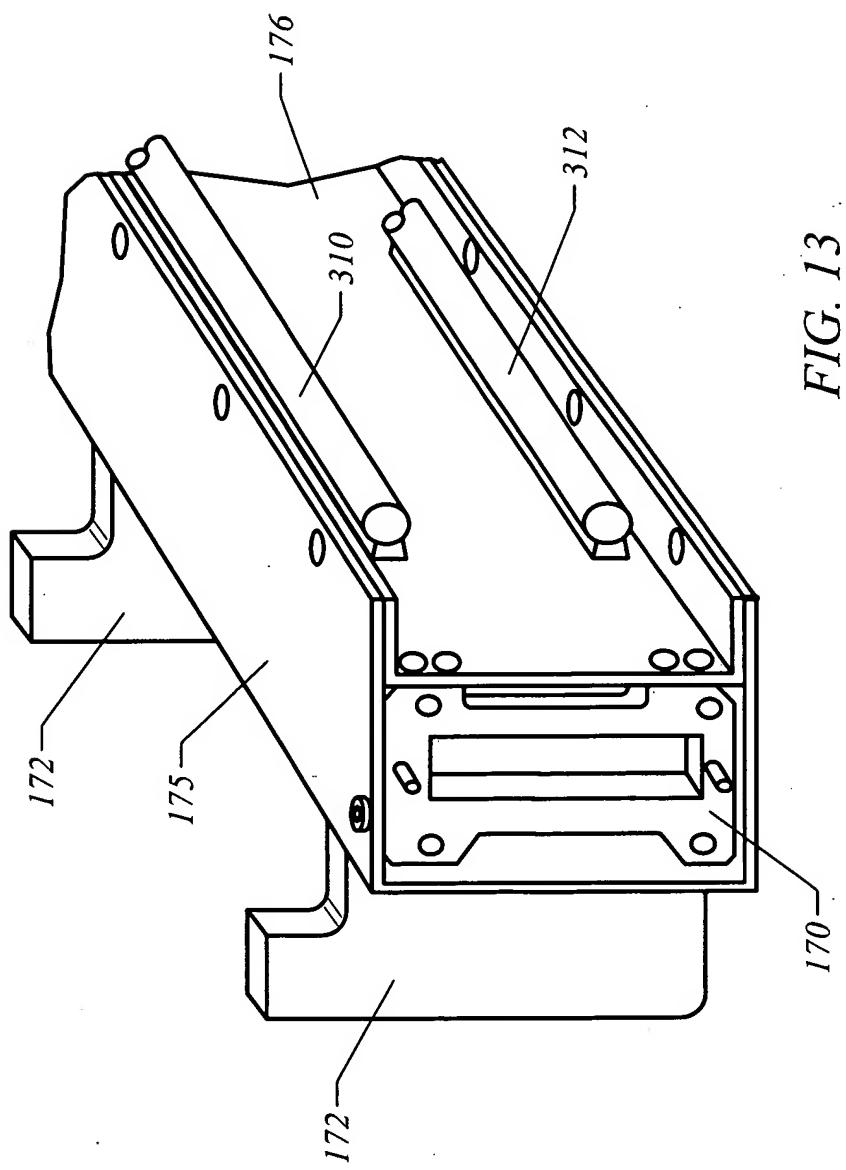


FIG. 13

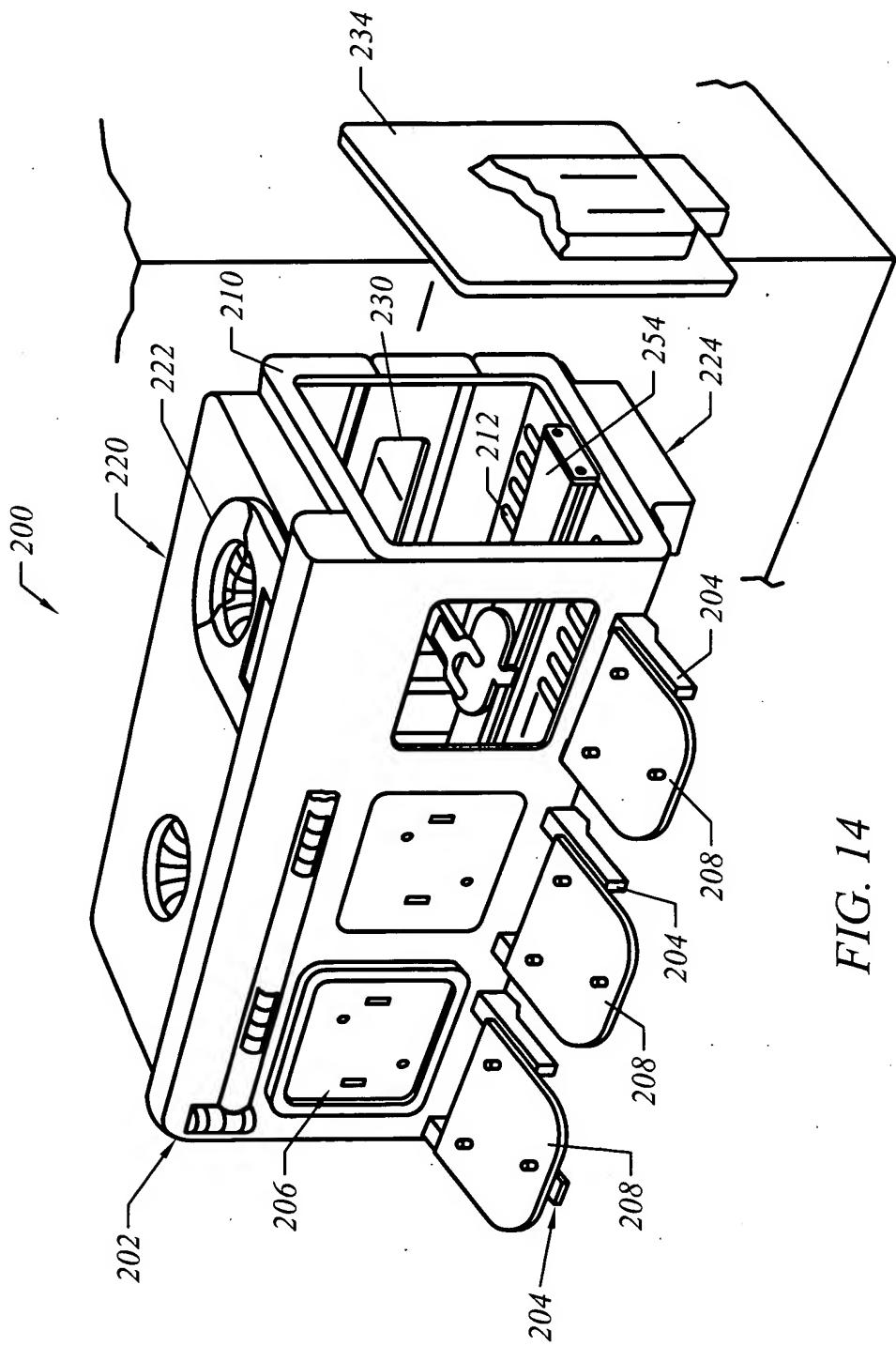


FIG. 14

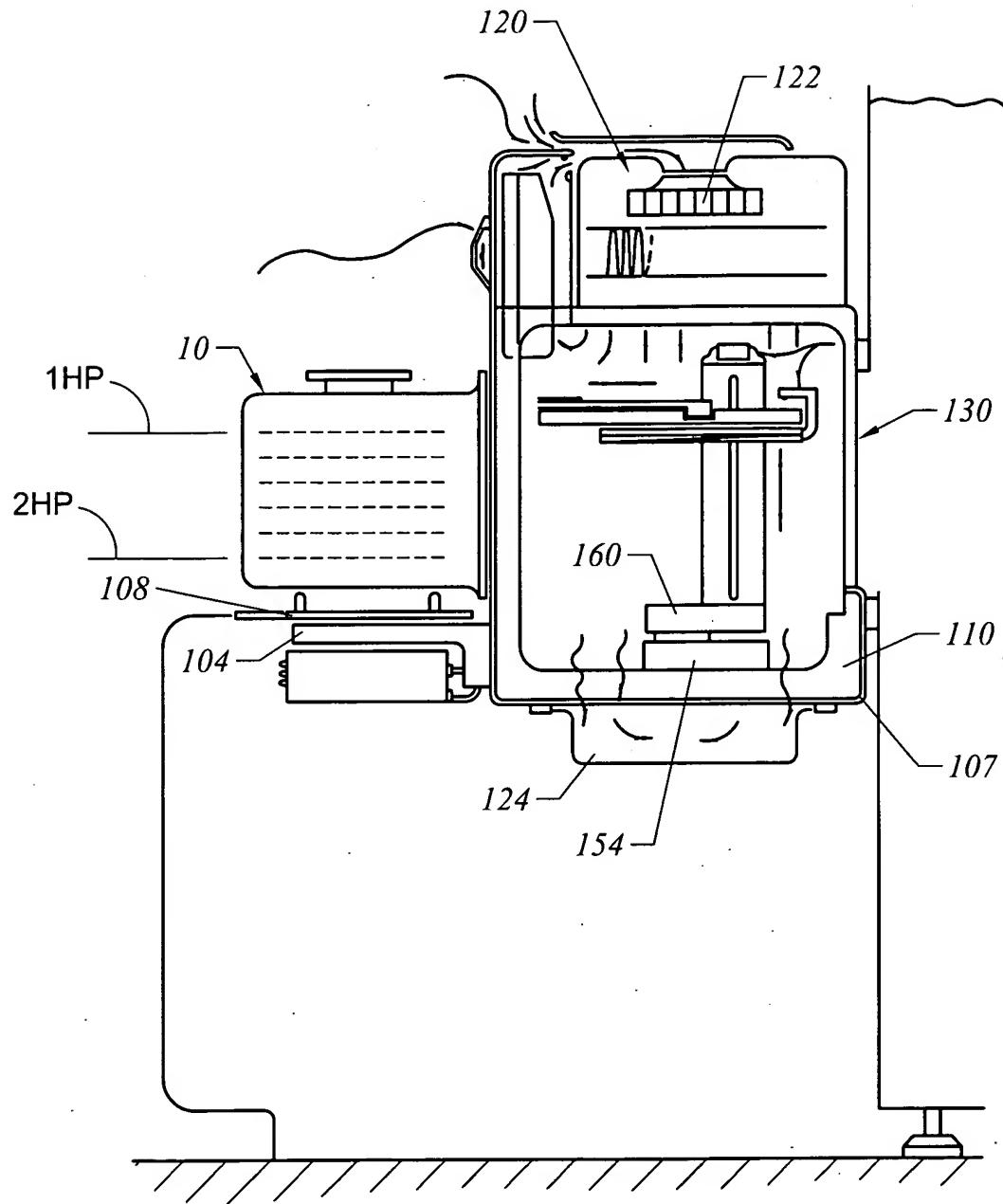


FIG. 15

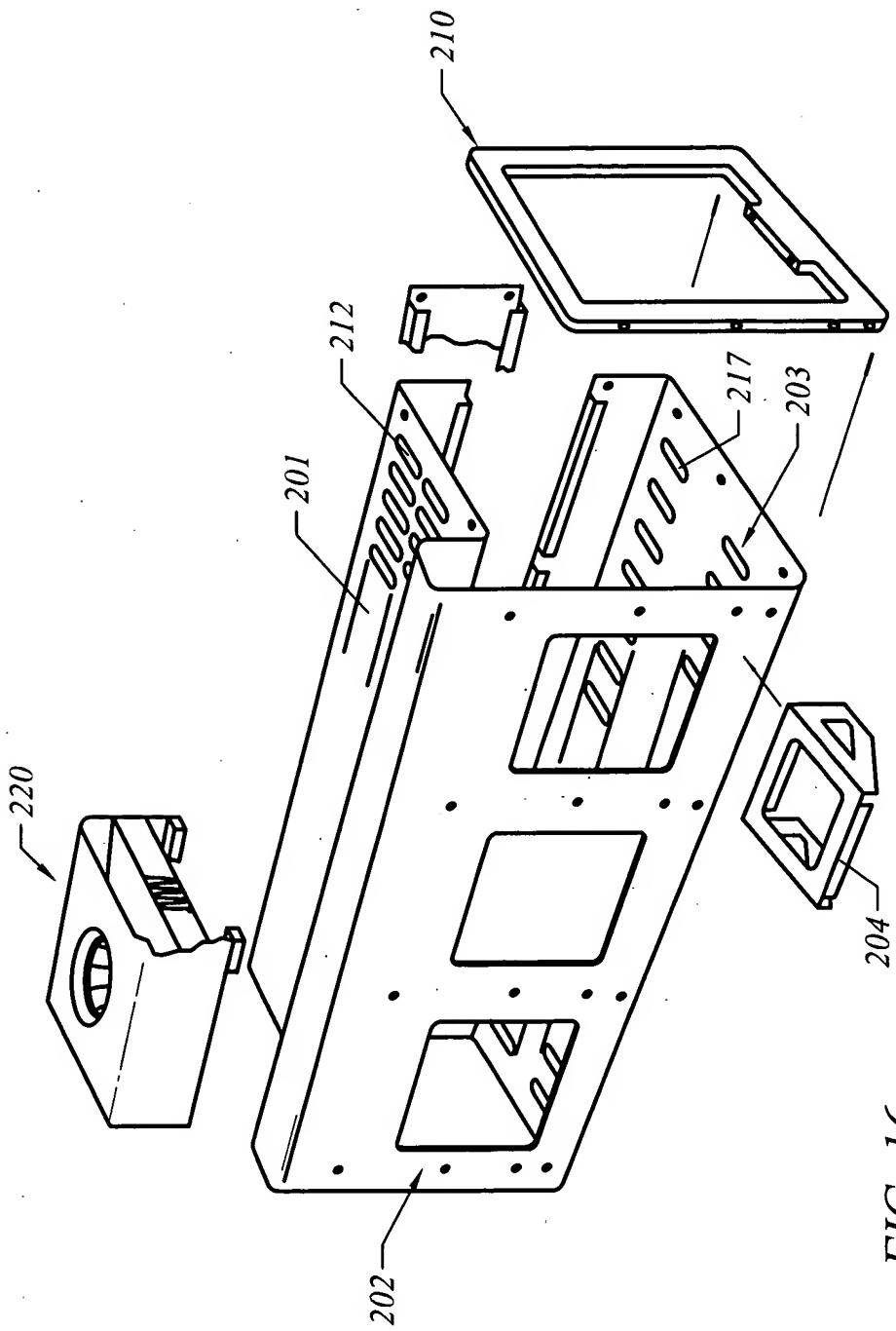


FIG. 16

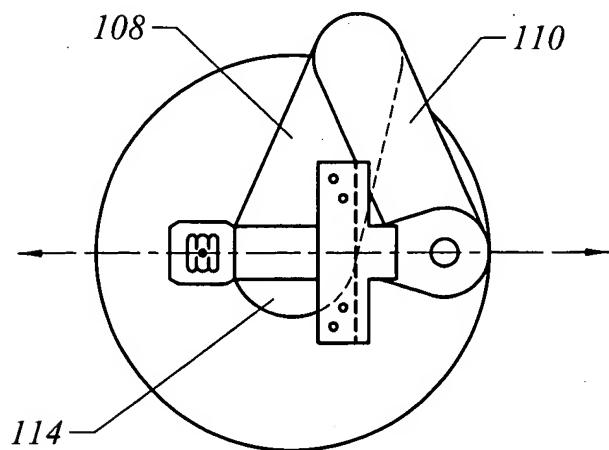


FIG. 17A
(*Prior Art*)

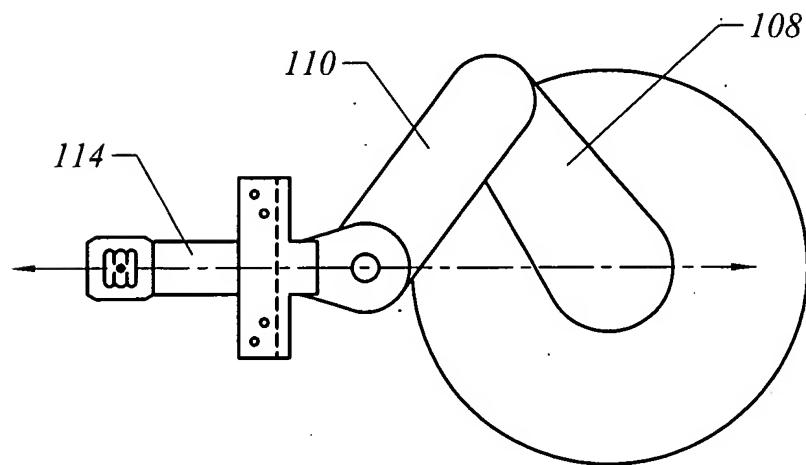


FIG. 17B
(*Prior Art*)

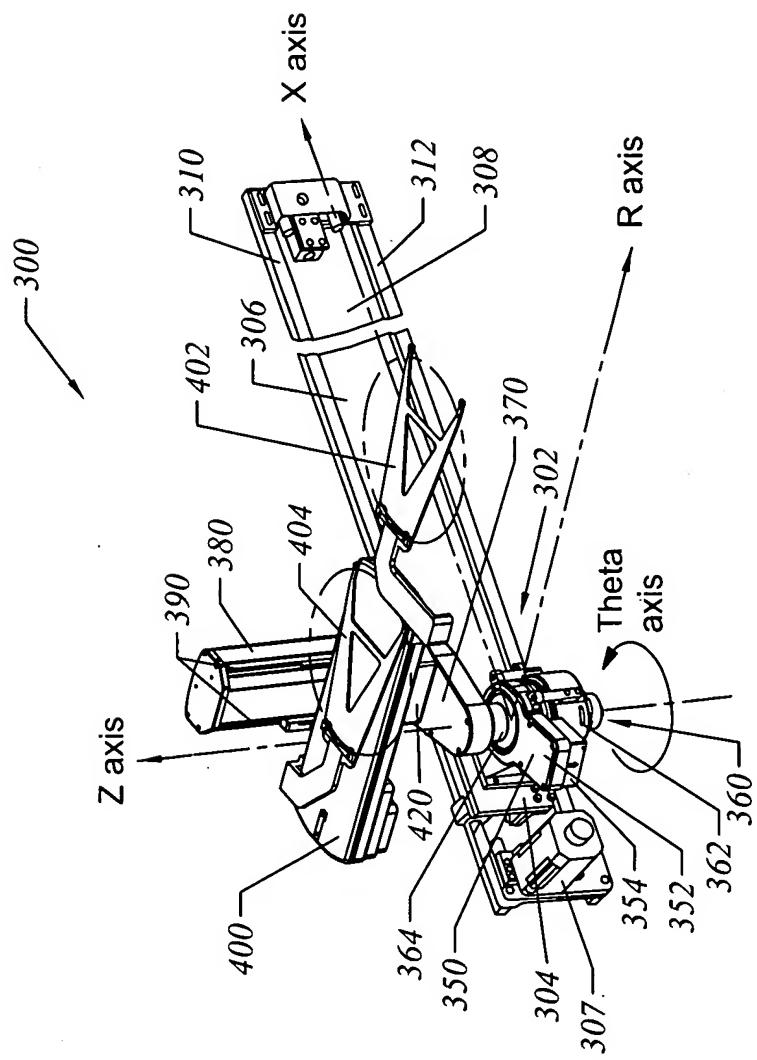


FIG. 18

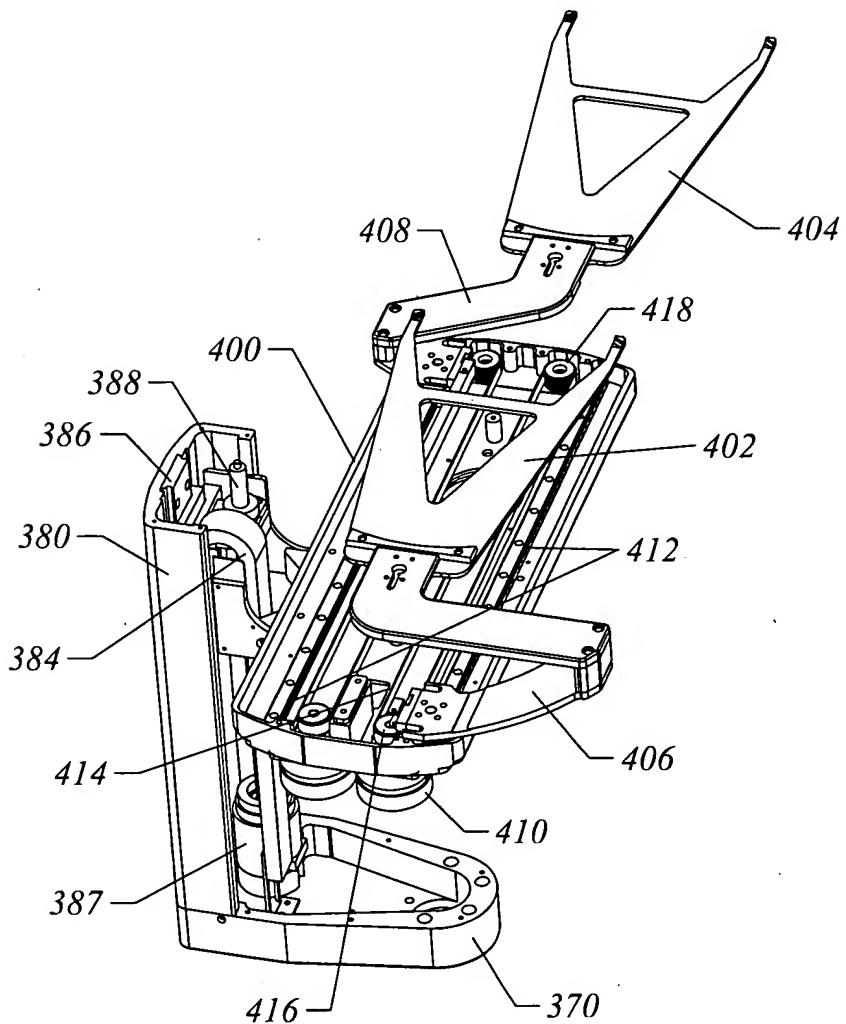


FIG. 19

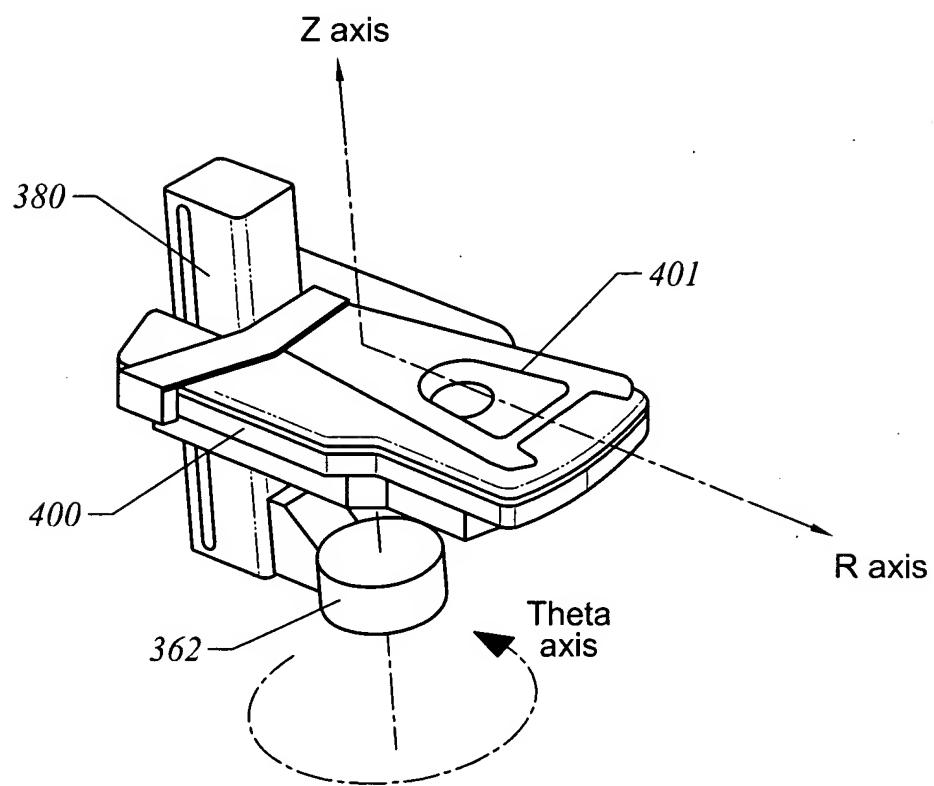


FIG. 20

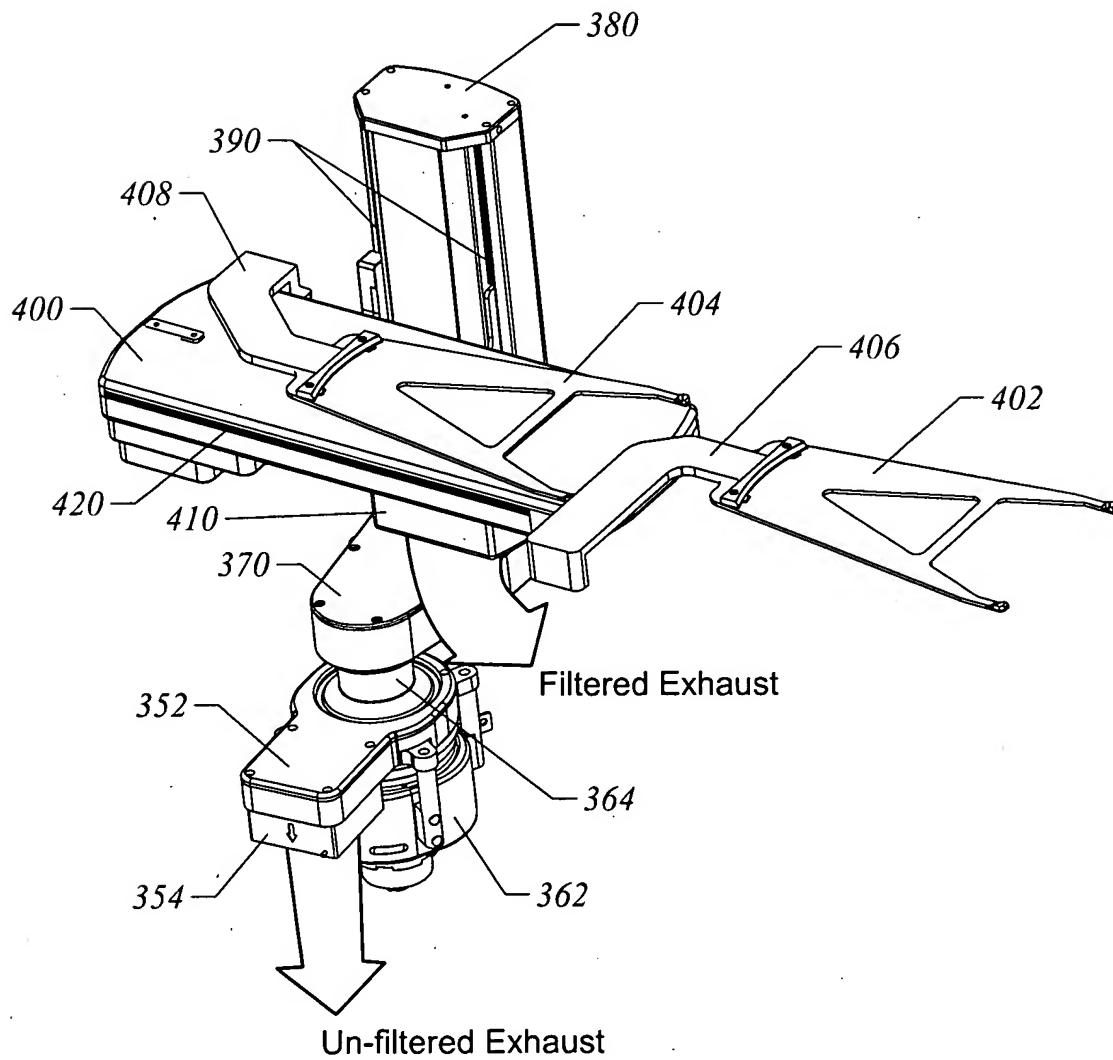


FIG. 21

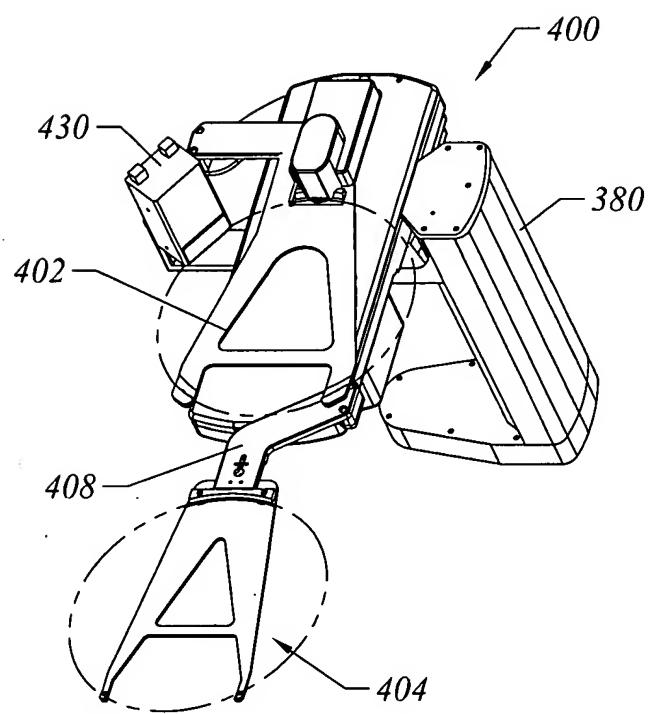


FIG. 22A

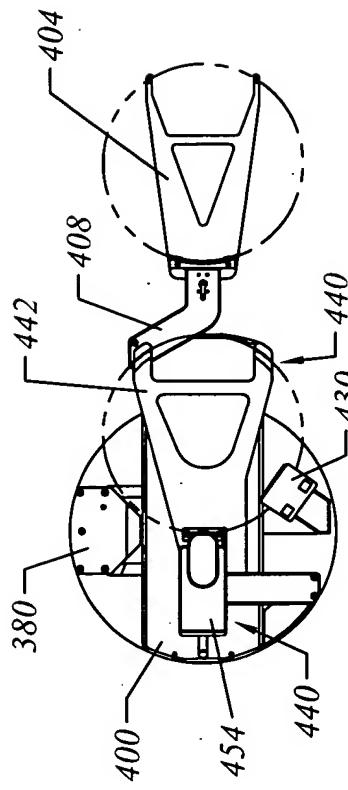


FIG. 22B

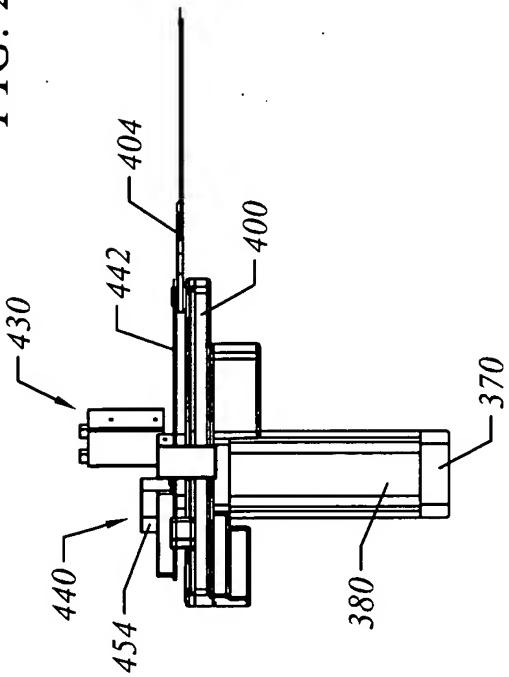


FIG. 22C

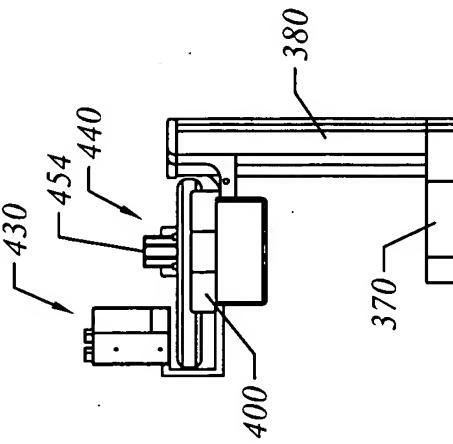


FIG. 22D

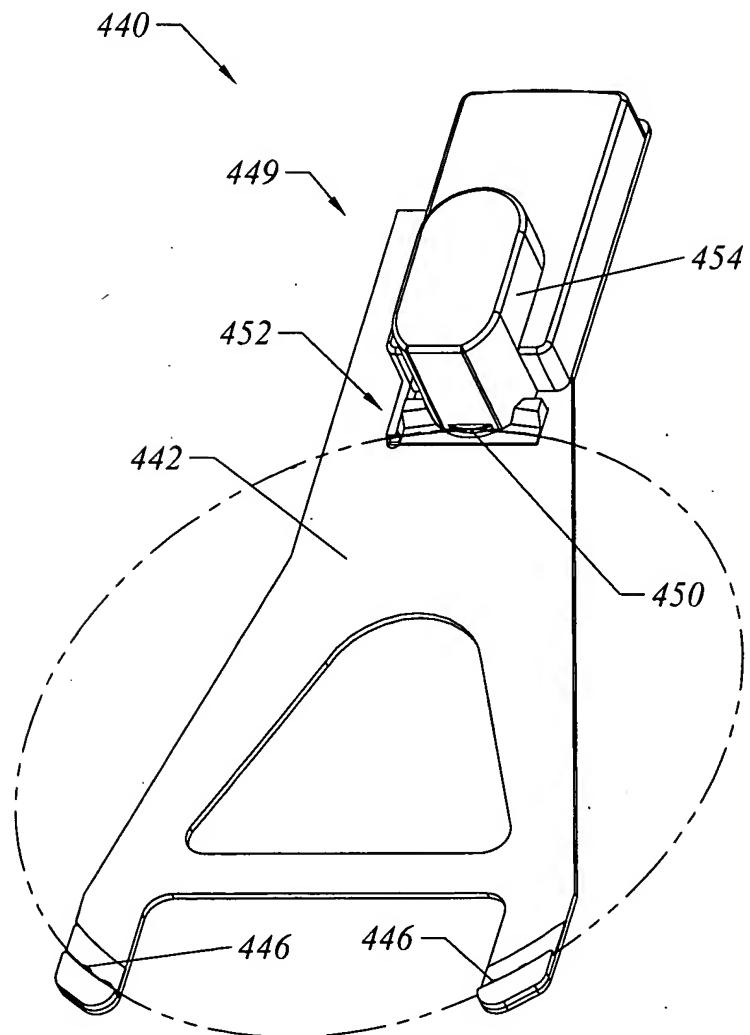


FIG. 23

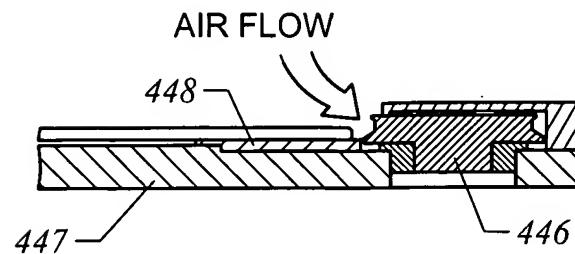


FIG. 24A

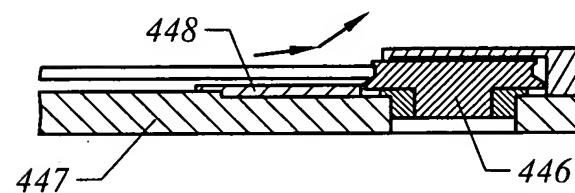


FIG. 24B

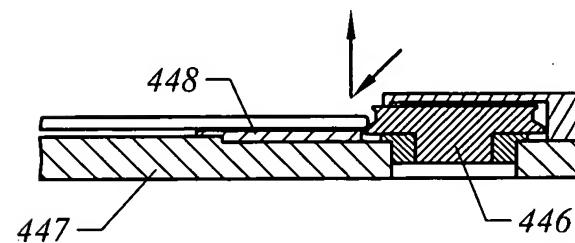


FIG. 24C

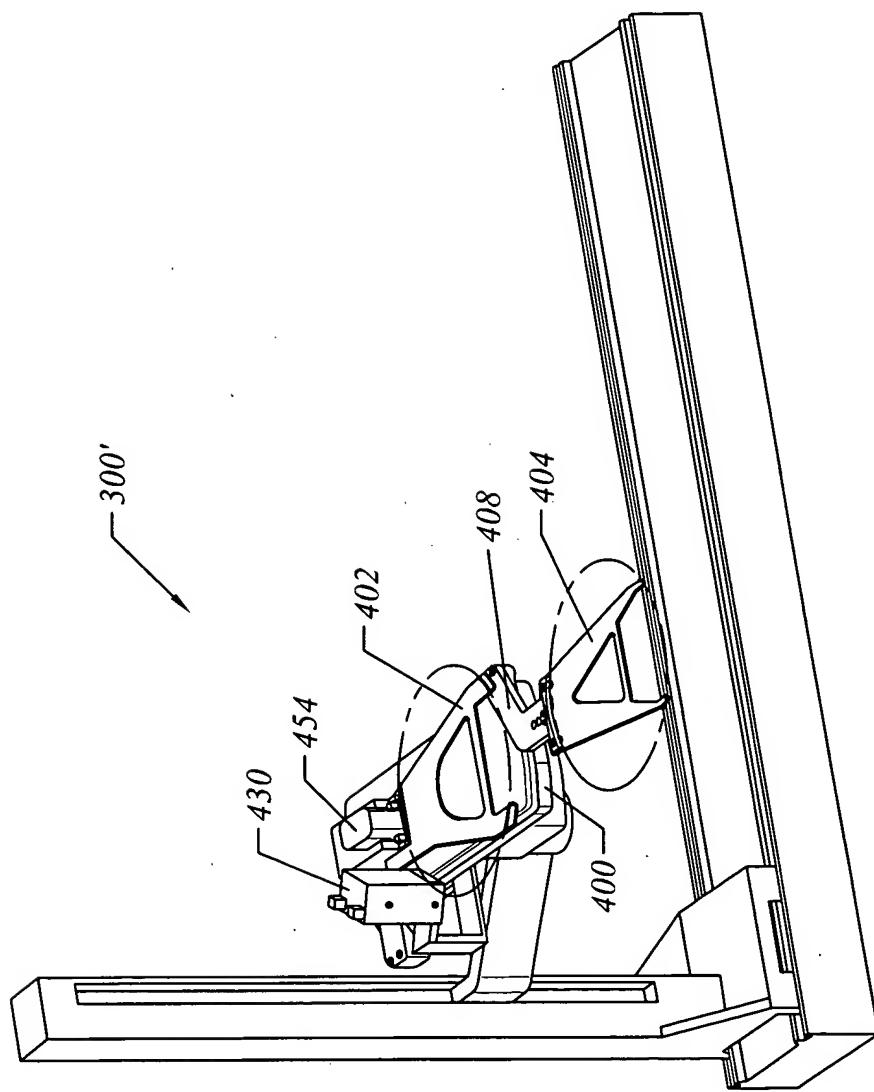


FIG. 25

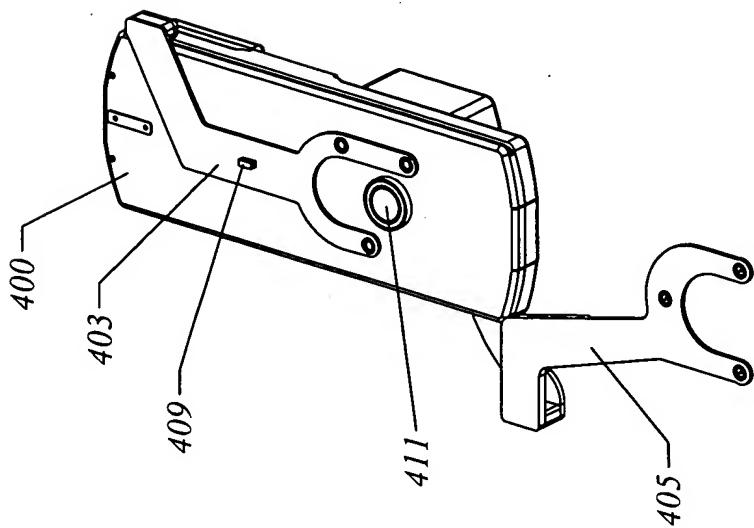


FIG. 26B

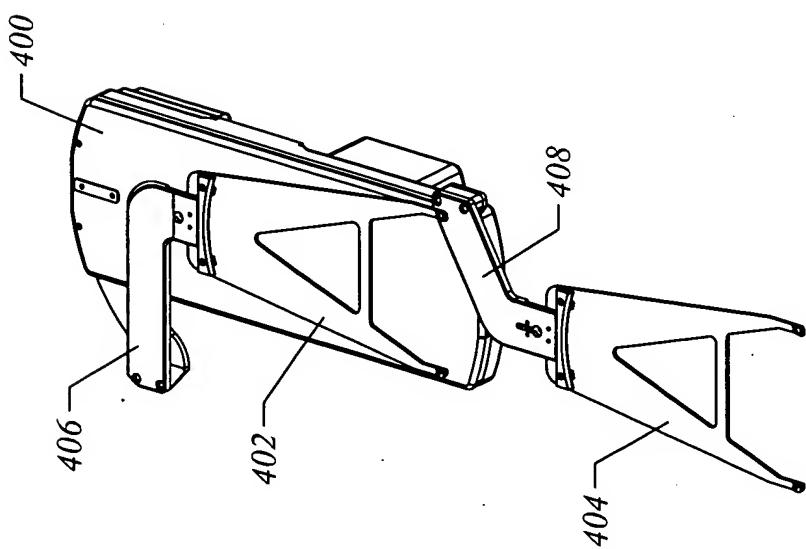


FIG. 26A

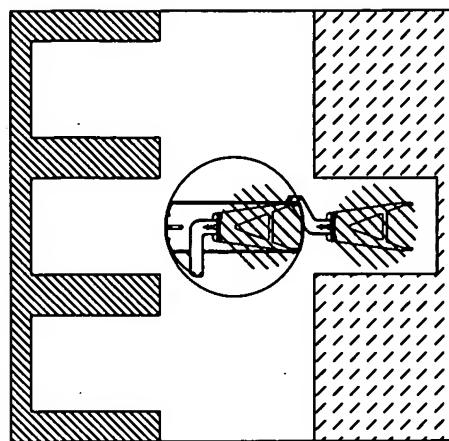


FIG. 27A

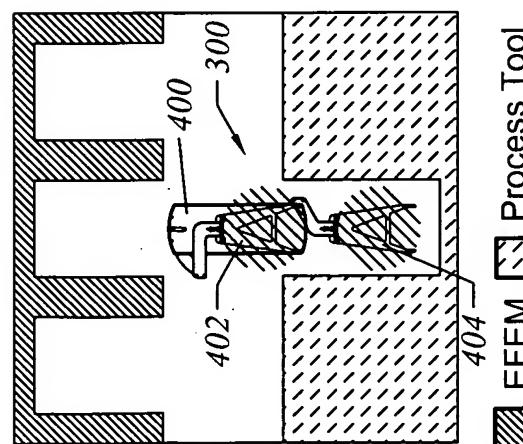


FIG. 27B

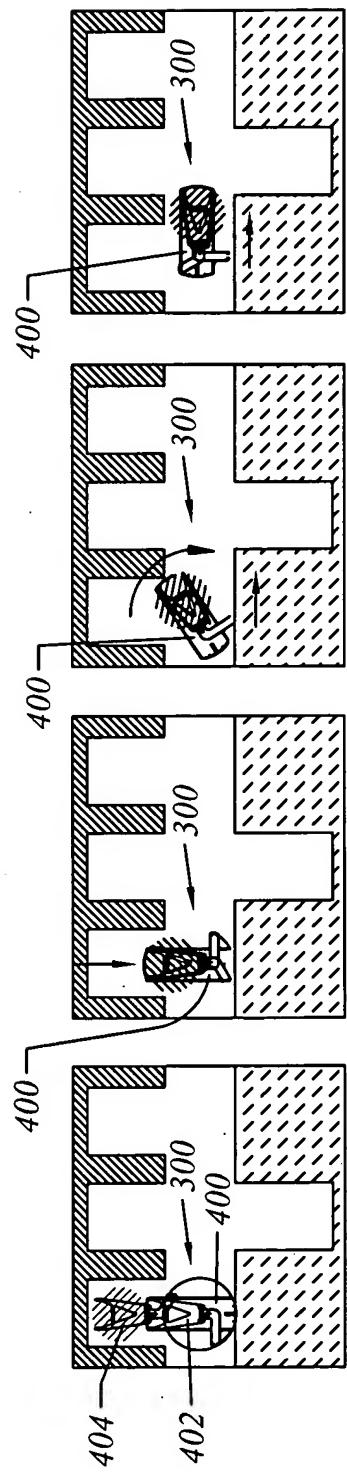


FIG. 28-1

FIG. 28-2

FIG. 28-3

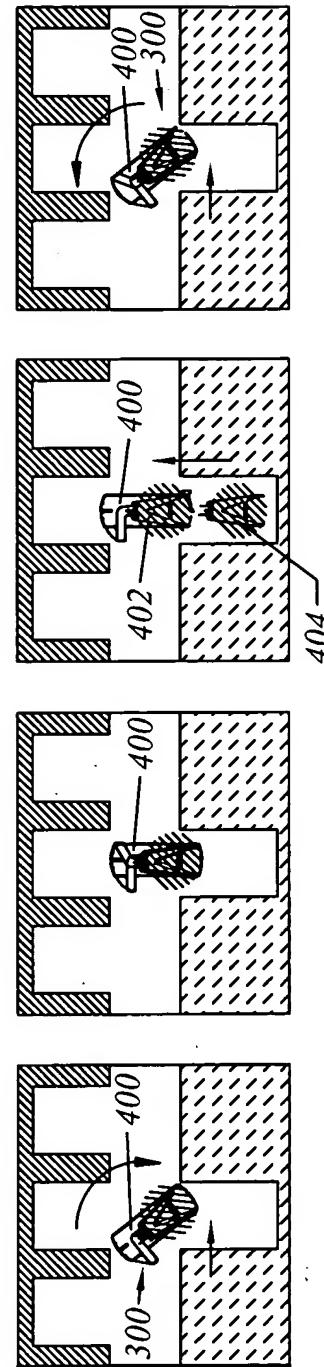


FIG. 28-4

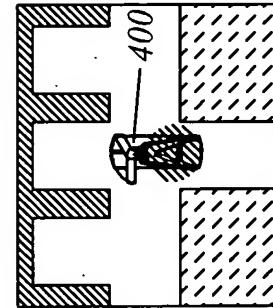
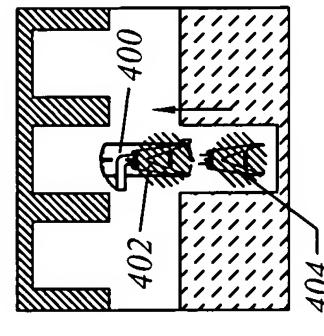


FIG. 28-8

FIG. 28-7

FIG. 28-6

FIG. 28-5

EFEM
Process Tool

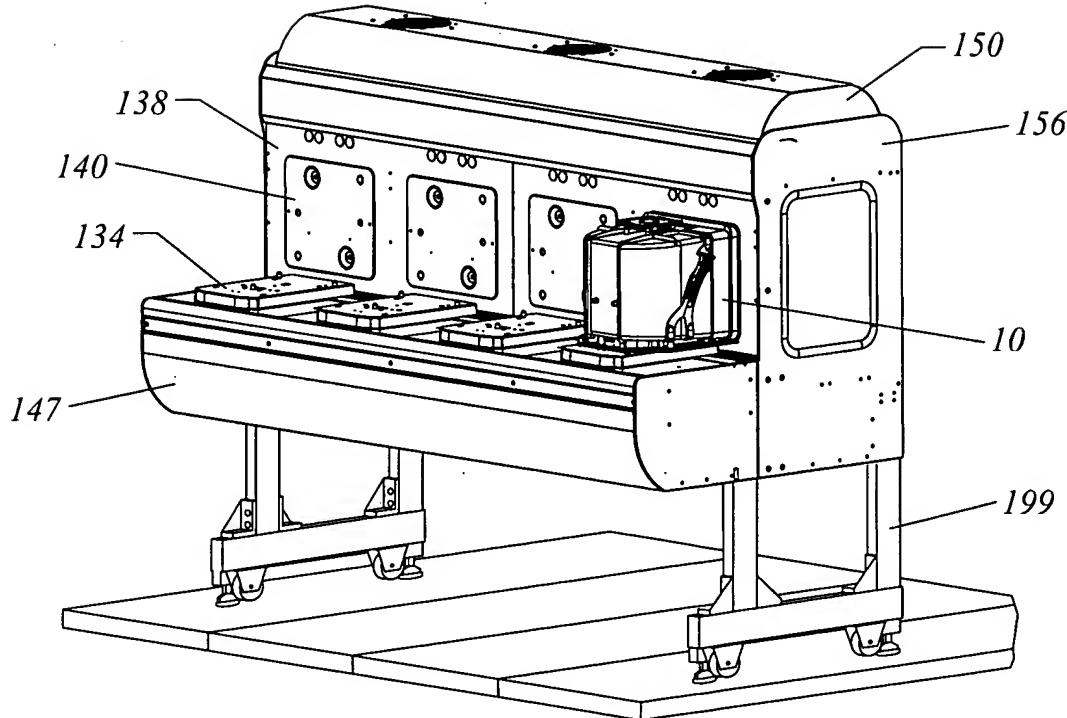


FIG. 29A

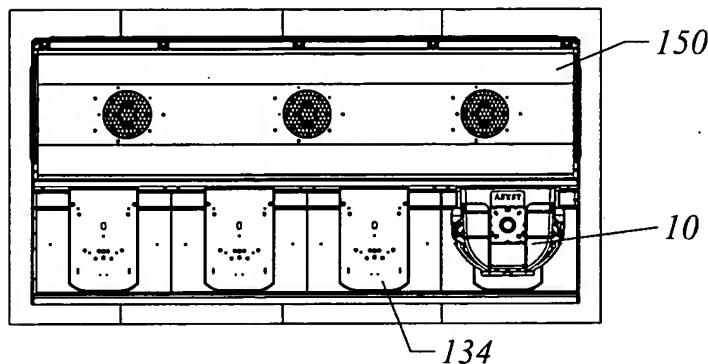


FIG. 29D

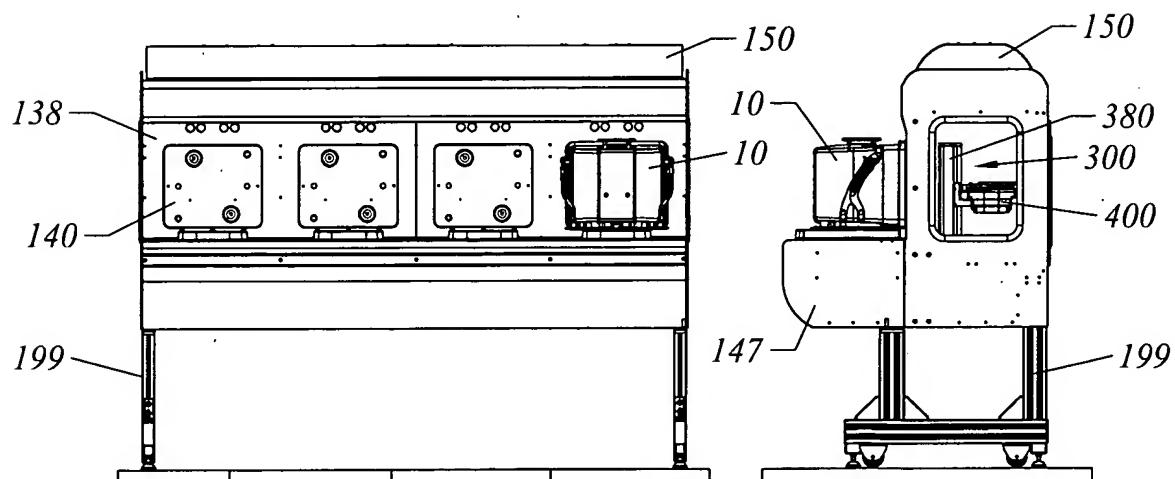


FIG. 29B

FIG. 29C

Title: *Unified Frame for Semiconductor Handling System*
Inventor: A. Bonora, et al. Docket No.: 34741-752
Serial No.: 10/087,638 Attorney: Scott D. Sanford
Filed: March 1, 2002 Telephone: 415-984-8700
Replacement Sheet 32 of 34

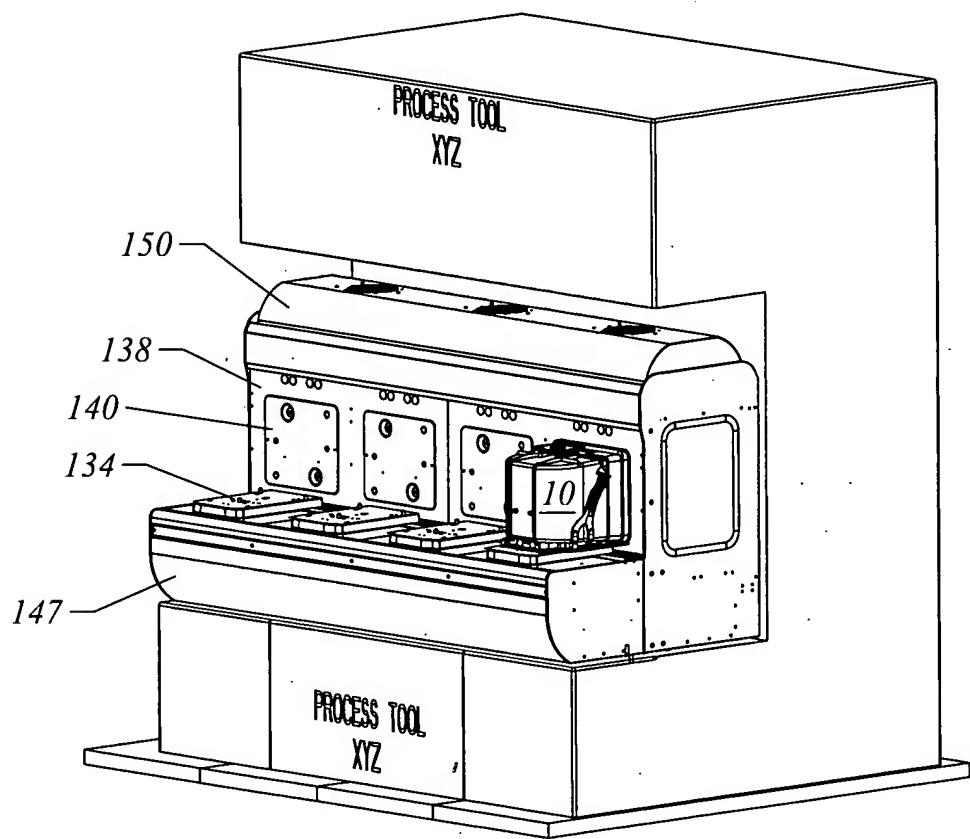


FIG. 30A

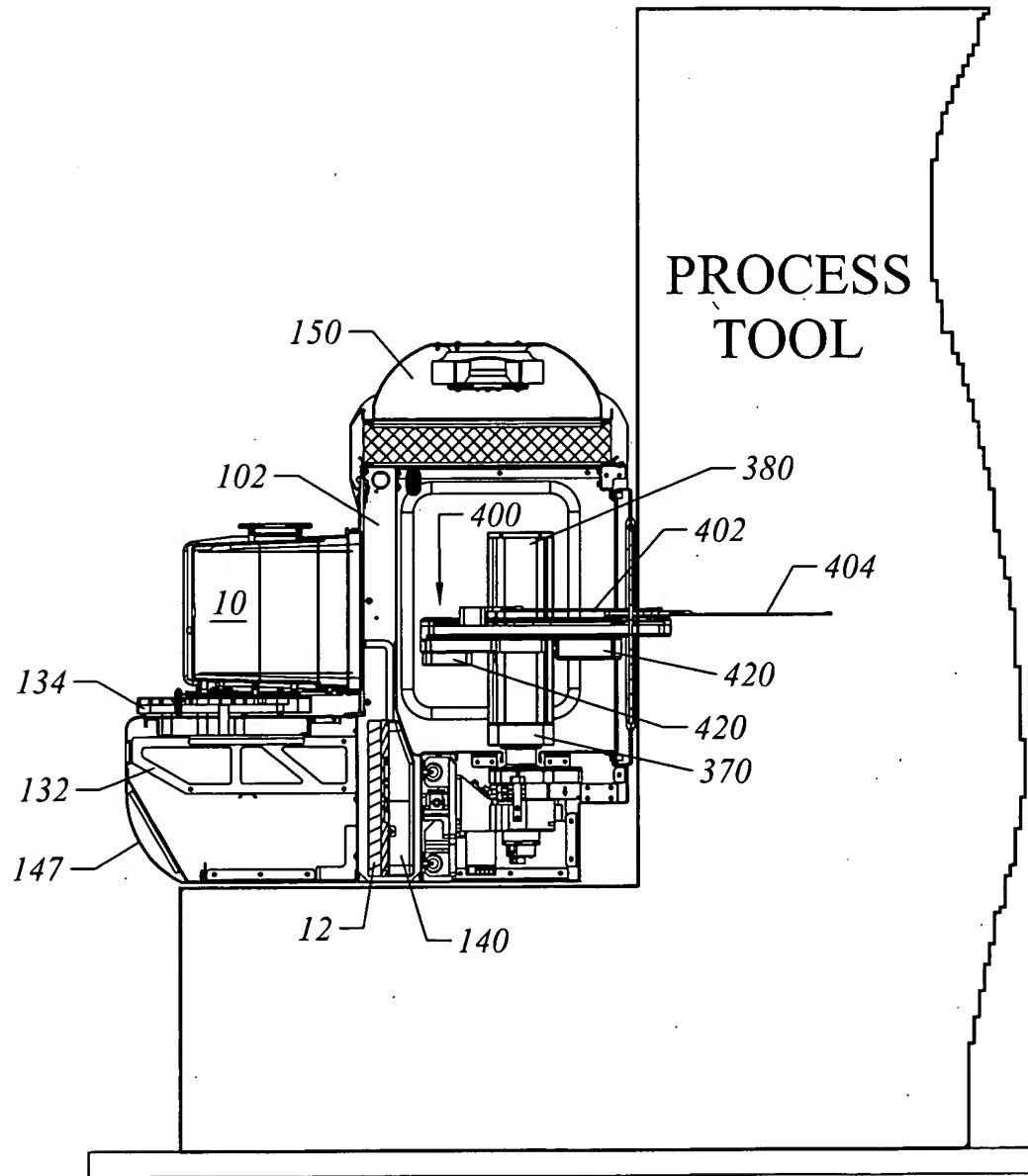


FIG. 30B

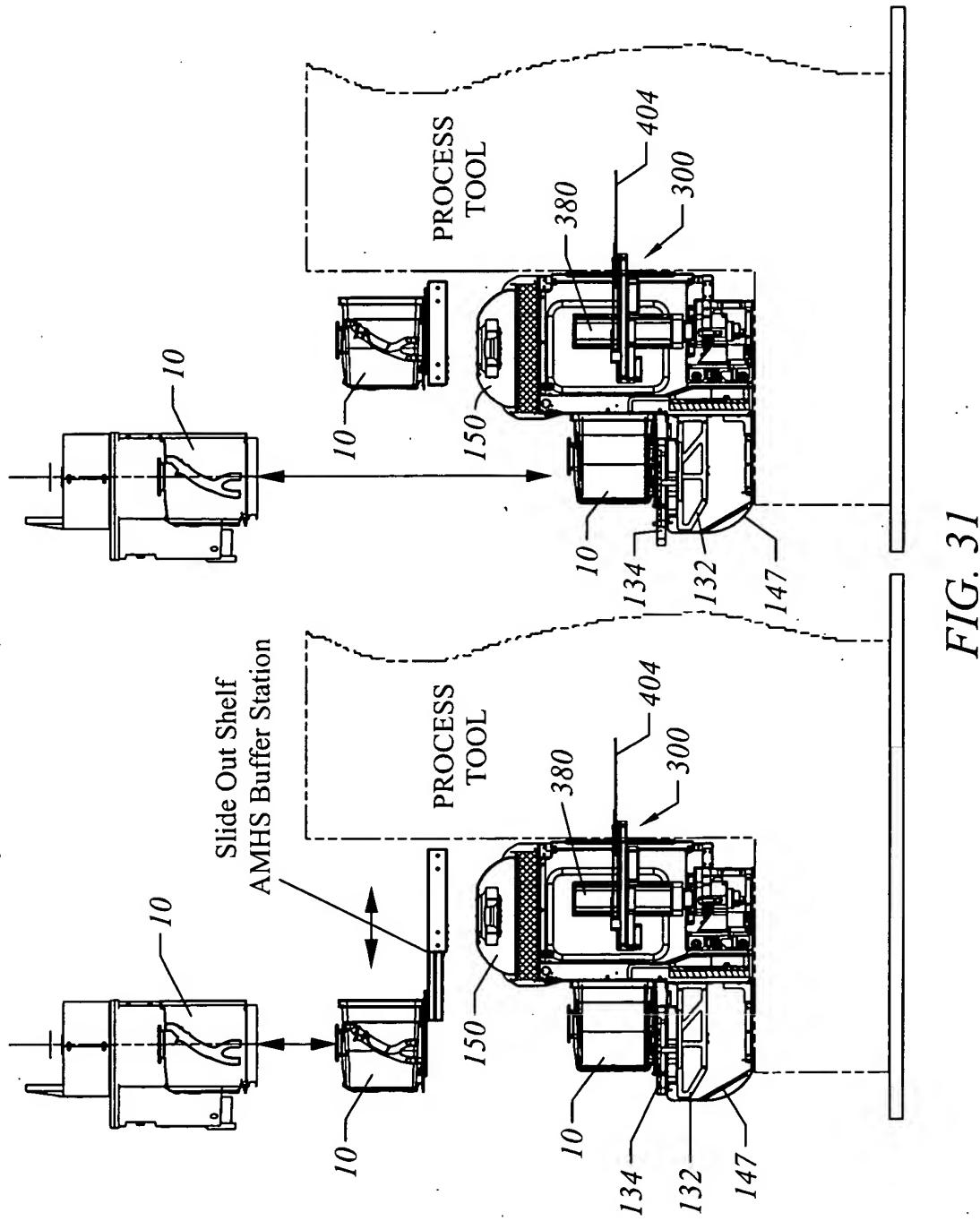


FIG. 31

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